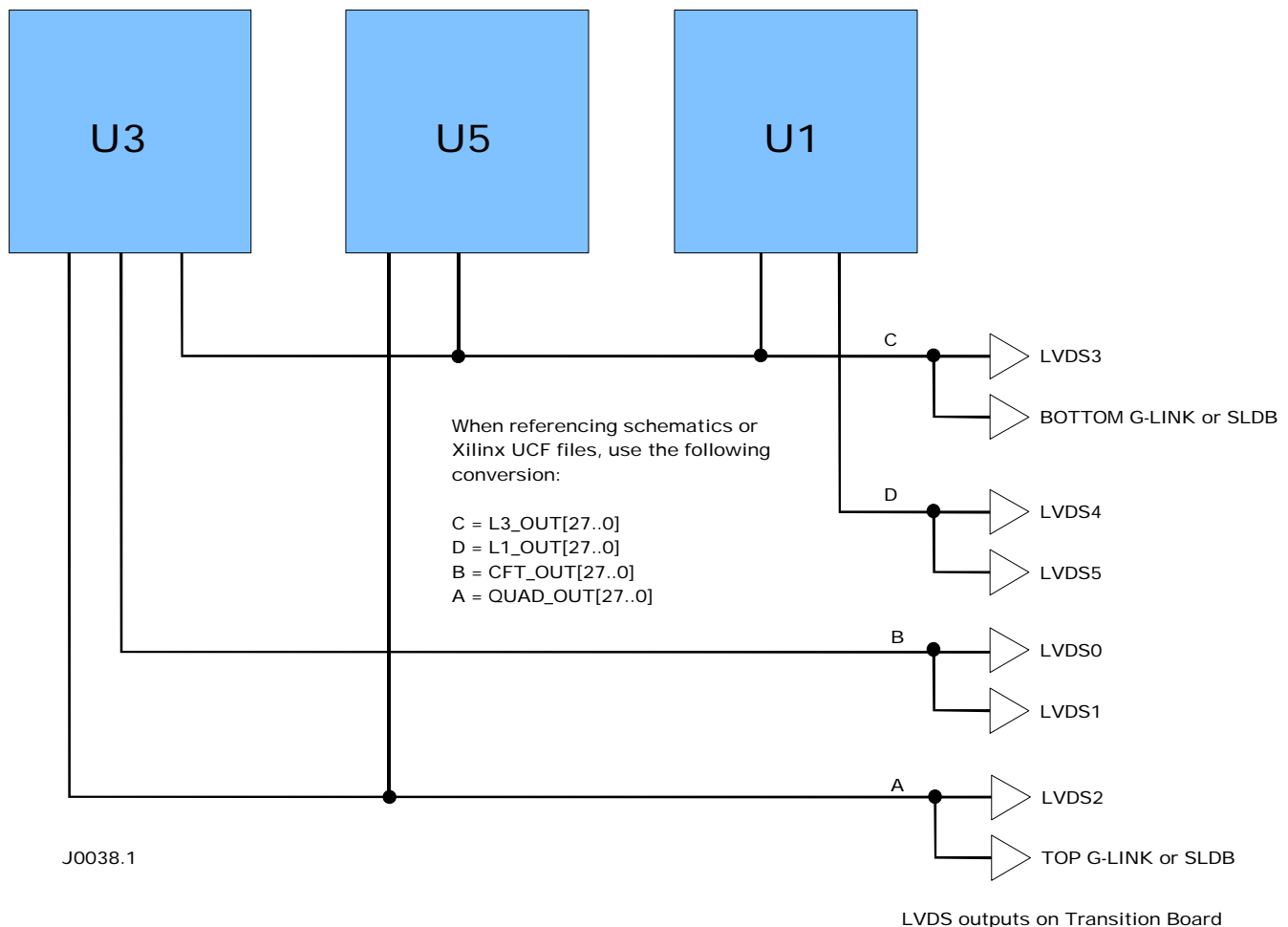**Jamieson T. Olsen**
Engineering Note

Date: 11 October 2001

Project: Double Wide Daughterboards
Doc. No: 2001-10-11a

Subject: Output Bus Structure for DWDBs

DFE Double-Wide Output Bus Structure

When referencing schematics or Xilinx UCF files, use the following conversion:

C = L3_OUT[27..0]
D = L1_OUT[27..0]
B = CFT_OUT[27..0]
A = QUAD_OUT[27..0]

J0038.1

LVDS outputs on Transition Board

NOTE: if a G-LINK or SLDB is installed on a bus, the LVDS transmitter on that bus cannot be used. LVDS output numbers 0-5 are based on the Transition Board schematic. Refer to my engineering note 2001-02-02a for a diagram of the rear connector on the Transition Board.